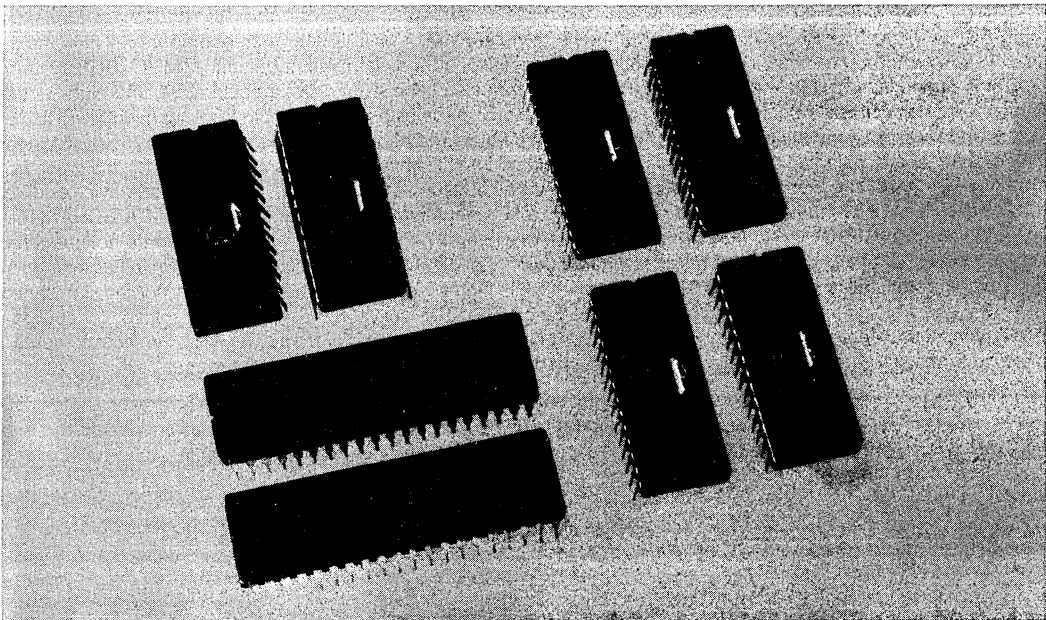


iSBC[®] 577 SPEECH TRANSACTION RECOGNITION CHIP SET

- High-volume solution for speech I/O
- Fully compatible with iSBC 570, 576-generated software

The iSBC[®] 577 Speech Recognition Chip Set is a solution for your high-volume/maximum value-added speech I/O solution. The Chip Set contains the Intel-developed proprietary components from the iSBC[®] 576 Speech Transaction Board. With these components you can build the equivalent of the Speech Transaction Board into your own system. The Chip Set contains the digital front-end processors, a preprogrammed 8048 interface processor, the Speech Transaction Manager Firmware on 27128 EPROMs, and the 8086 microprocessor.



SPECIFICATIONS

Performance

—Refer to iSBC[®] 570 and iSBC[®] 576 performances.

Equipment Supplied

- 2—Preprogrammed 2920/21s (Digital Front-end Processor)
- 4—Preprogrammed 27128 (Speech Transaction Manager)
- 1—Preprogrammed 8048 (Interface Processor)
- 1—8086

ORDERING INFORMATION

Part Number Description

iSBC [®] 577	Speech Transaction Recognition Chip Set
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